

K-No.: 26543	1 A Differential Current Sensor for 5V Supply Voltage For the electronic measurement of current: DC, AC, pulsed ..., with galvanic isolation between the primary and the secondary circuit		Date: 02.02.2017
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Description	Characteristics	Applications
<ul style="list-style-type: none"> Closed loop (compensation) Current Sensor with magnetic probe Printed circuit board mounting Casing and materials UL-listed 	<ul style="list-style-type: none"> excellent accuracy very low offset current very low temperature dependency and offset drift very low hysteresis of offset current short response time wide frequency bandwidth compact design reduced offset ripple 	Mainly used for stationary operation in industrial applications: <ul style="list-style-type: none"> Solar inverter

Electrical data - Ratings

I _{PN}	Primary nominal RMS current	85	A
I _{ΔN}	Differential rated RMS current	1.0	A
V _{OUT}	Output voltage @ I _{ΔP}	V _{REF} ± (1.2V * I _{ΔP} / I _{ΔN})	V
V _{OUT(0)} ¹	Output voltage @ I _{ΔP} =0A, θ _A =25°C	V _{REF} ± 0.015	V
V _{OUT(Error)}	in case of error (current sensor) V _{OUT} < 0.5V is set	< 0.5	V
V _{REF}	internal reference voltage @ I _{ΔP} =0A	2.5 ± 0.005	V
	external reference voltage range	1.4...3.5	V
V _{REF(test current)} ²	Reference voltage (external)	0 ... 0.1	V
V _{OUT(test current)} ²	Output voltage @ V _{REF} = 0...0.1V	V _{OUT(0)} + 0.25±0.06	V
K _N	Transformation ratio	1:1:1:1 : 20 : 1000	

¹ with switching on and after "test current" the sensor is degaussed by an internal AC-current for about 110ms.
In this time the output is set to V_{OUT} < 0.5V.

² If V_{REF} is set external to 0...0.1V an internal test current is generated.

Accuracy – Dynamic performance data		min.	typ.	max.	Unit
I _{ΔP,max}	Max. measuring range (differential current)	±1.7			A
X	Accuracy @ I _{PN} , θ _A = 25°C		1.5		%
ε _L	Linearity		1		%
V _O (V _{OUT} -V _{REF})	Offset voltage @ I _{ΔP} = 0A, θ _A = 25°C		15		mV
ΔV _{O/ΔT}	Temperature drift of V _{OUT} @ I _{ΔP} =0A, θ _A	0.05			mV/°C
t _r	Response time @ 90% of I _{ΔN}		40		μs
f _{BW}	Frequency bandwidth	DC...8			kHz

General data

θ _A	Ambient operation temperature	-40	85	°C
θ _S	Ambient storage temperature (acc to M3101)	-40	85	°C
m	Mass		105	g
V _c	Supply voltage	4.75	5	5.25
I _c	Supply current at I _{ΔP} = 0A and RT		15	mA
1, 2)S _{clear}	Clearance (component without solder pad)	3.9		mm
1, 2)S _{creep}	Creepage (component without solder pad)	4.5		mm
¹⁾ U _{sys}	System Voltage *determines impulse voltage acc. table 7		600	V _{RMS}
¹⁾ U _{AC}	Working voltage *acc. table 10		800	V _{RMS}
¹⁾ U _{PD}	Rated discharge voltage *acc. table 24 with U _{PD} =U _{AC} *√2		1132	V _{PEAK}

¹⁾Constructed and manufactured and tested in accordance with IEC 61800-5-1:2007

Prim - Prim: Functional Insulation, Prim - Sec: Basic Insulation,

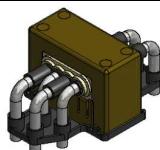
Insulation material group 1, Pollution degree 2, Overvoltage category III

²⁾According to customers specification

Date	Name	Issue	Amendment
02.02.2017	DJ	82	Page A1, M-sheet M3101 added to the storage temperature line. CN-17-007
Hrg.: MC-PD editor	Bearb.: DJ designer	MC-PM: ZP check	freig.: BEF released

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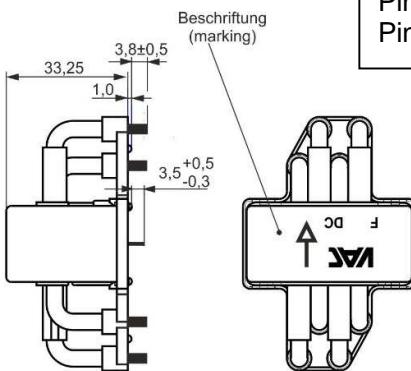
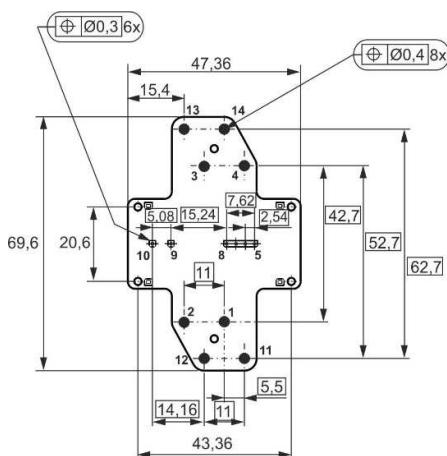
Customer: Standard Type

Customers Part no:

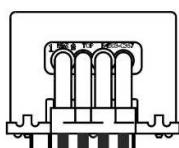
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Mechanical outline (mm):**General tolerances DIN ISO 2768-c**

Connections:

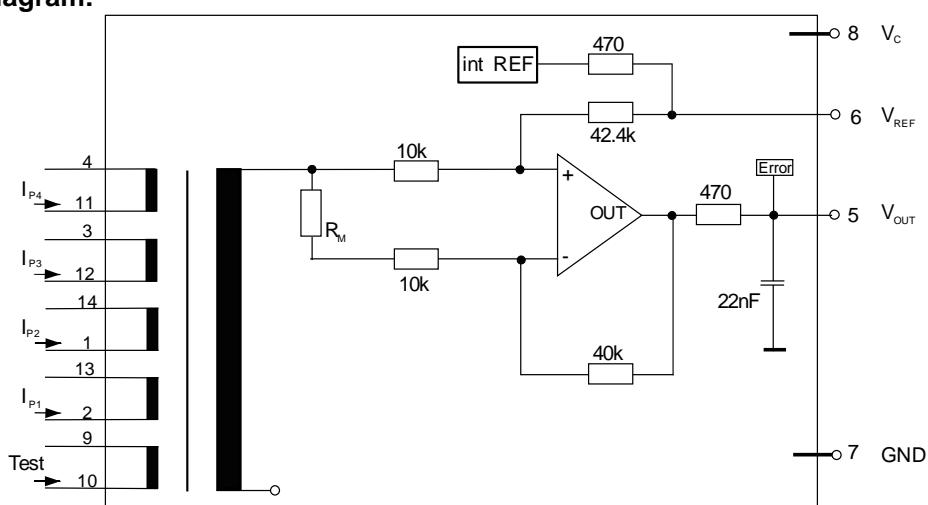
Pin 5-10: 0.7mm x 0.7mm
Pin 1-4, 11-14: Ø4.5mm


Marking:

VAC
4646-X931
F DC

Prüfmaß
(test dimension)

Ohne Maßstab gezeichnet
(without scale drawn)

DC = Date Code
F = Factory

Current direction: A positive output voltage appears at point V_{OUT},
if primary current flows in direction of the arrow.
Schematic diagram:Hrg.: MC-PD
editorBearb.: DJ
designerMC-PM: ZP
checkfreig.: BEF
released

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Electrical data: (investigate by a type checking)		min.	typ.	max.	Unit
$V_{C,max}$	maximum supply voltage (without function)		6		V
I_C	Supply current with primary current		$16mA + I_{\Delta P} \cdot K_N + V_{OUT}/R_L$		mA
$I_{OUT,SC}$	Short circuit output current		± 20		mA
R_S	Secondary coil resistance @ $\theta_A = 85^\circ C$			80	Ω
R_{Test}	Test winding resistance @ $\theta_A = 25^\circ C$		0.9		Ω
$R_{P1,P2}$	Primary wire resistance @ $\theta_A = 25^\circ C$		0.1		$m\Omega$
$R_{i,REF}$	Internal resistance of reference input		470		Ω
$R_{i,OUT}$	Output resistance of V_{OUT}		470		Ω
$\Delta X_{Ti}/\Delta T$	Temperature drift of X @ $\theta_A = -40^\circ C \dots 85^\circ C$			400	ppm/K
$\Delta V_{REF}/\Delta T$	Temperature drift of V_{REF} @ $\theta_A = -40^\circ C \dots 85^\circ C$		5	50	ppm/K
$\Delta V_o = \Delta(V_{OUT}-V_{REF})$	Sum of any offset drift including:		30		mV
V_{Ot}	Long term drift of V_o		10		mV
V_{OT}	Temperature drift of V_o @ $\theta_A = -40^\circ C \dots 85^\circ C$		10		mV
$\Delta V_o/\Delta V_c$	Supply voltage rejection ratio		20		mV/V
V_{OH}	Hysteresis of V_{OUT} @ $I_{\Delta P} = 0$ (after an overload of 1000x $I_{\Delta N}$)	125	250		mV
$V_{OH, Demag}$	Hysteresis after Degaussing	40			
V_{OSS}	Offsetripple (without external filter)		150		mV
V_{OSS}	Offsetripple (with 20 kHz-Filter, first order)	25			mV
V_{OSS}	Offsetripple (with 1.6 kHz-Filter, first order)	10			mV
	Mechanical stress according to M3209/3 Settings: 10-2000Hz, 1min/Octave, 2 hours		2		g

Routine Tests: (Measurement after temperature balance of the samples at room temperature, SC=significant characteristic)

V_{OUT} (SC)	(100%) M3011/6:	Output voltage vs. reference	1182 ... 1218	mV
V_o	(100%) M3226:	Offset voltage ($V_{OUT}-V_{REF}$)	15	mV
U_d	(100%) M3014:	Test voltage, 1s, Pin 1-4 vs. Pin 5-10, *acc. table 21	2.0	kV_{RMS}
U_{PDE}	(AQL 1/S4)	Partial discharge voltage (extinction)	1.2	
$U_{PDE} \cdot 1.875$	M3024: *acc. table 24	Pin 1-4 vs. Pin 5-10	1.5	kV_{RMS}

Requalification Tests: (replicated every year, Precondition acc. to M3236)

\hat{U}_W	M3064	Impulse test (1.2 μ s/50 μ s wave form) Pin 1-4 vs. Pin 5-10	6	kV
$\hat{U}_{W, prim-prim}$	M3064	Impulse test (1.2 μ s/50 μ s wave form) Pin 1 vs. Pin 11,12 and Pin 12 vs. Pin 1,2	6	kV
U_d	M3014	Test voltage, 5s Pin 1-4 vs. Pin 5-10	2.0	kV_{RMS}
$U_{d, prim-prim}$	M3014	Test voltage between primary conductors, 5s Pin 1 vs. Pin 11,12 and Pin 12 vs. Pin 1,2	2.0	kV_{RMS}
U_{PDE}	M3024	Partial discharge voltage (extinction)	1.2	
$U_{PDE} \cdot 1.875$		*acc. table 24	1.5	kV_{RMS}

* IEC 61800-5-1:2007

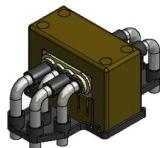
Other instructions

- Temperature of the primary conductor should not exceed 100°C.
- Housing and bobbin material UL-listed: Flammability class 94V-0.

Hrg.: MC-PD editor	Bearb.: DJ designer		MC-PM: ZP check			freig.: BEF released
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Explanation of several terms used in the tables:
V_{ot} Long term drift of V_O after 100 temperature cycles in the range -40°C to 85°C.

t_r Response time, measured as a delay time at I_{ΔP} = 0.9 * I_{ΔN} between a rectangular primary current and the output current or voltage.

t_{ra} Reaction time, measured as a delay time at I_{ΔP} = 0.1 * I_{ΔN} between a rectangular primary current and the output current or voltage.

X_{ges(I_{ΔN})} The sum of all possible errors over the temperature range by measuring a current I_{ΔN}:

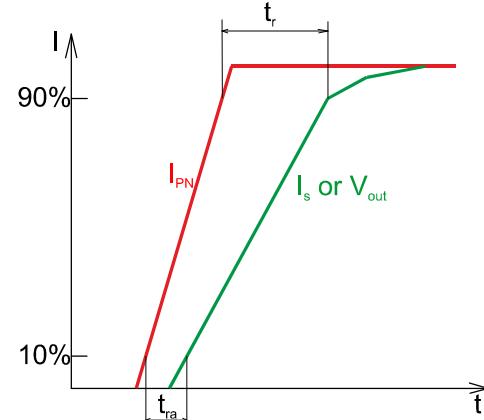
$$X_{\text{ges}(I_{\Delta N})} = 100 * \left| \frac{V_{\text{OUT}}(I_{\Delta N}) - 2.5V}{1.2V} - 1 \right| \%$$

X Permissible measurement error in the final inspection at RT, defined by

$$X = 100 * \left| \frac{V_{\text{OUT}}(I_{\Delta N}) - V_{\text{OUT}}(0)}{1.2V} - 1 \right| \%$$

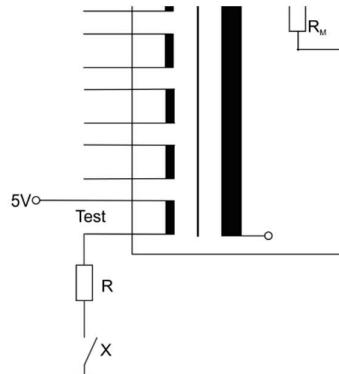
ε_L Linearity fault defined by: ε_L = 100 * $\left| \frac{I_{\Delta P}}{I_{\Delta N}} - \frac{V_{\text{OUT}}(I_{\Delta P}) - V_{\text{OUT}}(0)}{V_{\text{OUT}}(I_{\Delta N}) - V_{\text{OUT}}(0)} \right| \%$

 Where I_P is any input DC current and V_{OUT} the corresponding output term. (I₀ = 0).

RT Room temperature

Application Information

The external test current can be generated with the use of a resistor R and a switch X or something similar (Transistor, Mosfet, etc.). The resistor determine the current at a given voltage and so the output voltage can be calculated.

$$V_{\text{OUT}} = V_{\text{REF}} + \frac{1.2 \cdot \frac{5V}{R + R_{\text{Test}}} \cdot 20}{I_{\Delta N}}$$

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